

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts
 Pending
 Active
 L1: (198) ("4" or four) adj input\$1 adj adder
 L2: (1) 1.ti.
 L4: (10) 1 same 3
 L5: (2) ("20030018676").PN.
 L3: (1158) ("4" or four) adj multipliers
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Search Edit Browse Query Clear
 DB3 US:PCPUB:USPATRUSOCR:EPOR:PODERVENT:IBMLTOR
 Default operator ADJ
 Highlight all hit terms initially

1 same 3

BRS form RIS form Image Text HTML

	U	(1)	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cl	Inventor	S	C
1			US 5974582 A	19991026	12	High-speed chien search logic	714/781	708/492; 714/784		Ly, Davis M.		
2			US 5943348 A	19990824	13	Method to check for burst limiting in error correcting systems	714/761	714/752; 714/774		Ly, Davis M.		
3			US 6243732 B1	20010605	35	Data processor and data processing system	708/520	708/501; 708/505; 708/507		Arakawa, Fumio et al.		
4			US 6327605 B1	20011204	36	Data processor and data processing system	708/501	708/505; 708/507		Arakawa, Fumio et al.		
5			US 6038582 A	20000314	36	Data processor and data processing system	708/501	708/505; 708/507		Arakawa, Fumio et al.		
6			US 20010011291 A	20010802	37	Data processor and data processing system	708/495	708/501; 708/514		Arakawa, Fumio et al.		
7			US 3249743 A	19860503	47	Conditional variable incremental computer	708/494			MERZ DONALD M et al		

Ready NUM